# **General Purpose Silicon Rectifiers**

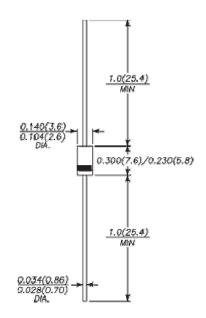
Reverse Voltage :50 to 1000 V Forward Current :1. 5 A

#### **FEATURES**

- ◆ The plastic package carries Underwrite s Laboratory Flammability Classification 94V-0
- Construction utilizes void-free molded plastic technique
- ◆ Low reverse leakage
- High forward surge current capability
- ◆ High temperature soldering guaranteed:
   260 C/10 seconds,0.375"(9.5mm) lead length, 5 lbs.
   (2.3kg) tension

#### **MECHANICAL DATA**

- Case: JEDEC DO-15 molded plastic body
- Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Mounting Position:Any
- Weight: 0.014 ounce, 0.40 grams



Dimensions in inches and (millimeters)

## Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

| Parameter  | Symbols            | 1N<br>5391  | 1N<br>5392 | 1N<br>5393 | 1N<br>5394 | 1N<br>5395 | 1N<br>5396 | 1N<br>5397 | 1N<br>5398 | 1N<br>5399 | Units |
|--|--------------------|-------------|------------|------------|------------|------------|------------|------------|------------|------------|-------|
| Maximum Repetitive Peak Reverse Voltage  | $V_{RRM}$          | 50          | 100        | 200        | 300        | 400        | 500        | 600        | 800        | 1000       | V     |
| Maximum RMS voltage  | $V_{RMS}$          | 35          | 70         | 140        | 210        | 280        | 350        | 420        | 560        | 700        | ٧     |
| Maximum DC Blocking Voltage  | $V_{DC}$           | 50          | 100        | 200        | 300        | 400        | 500        | 600        | 800        | 1000       | V     |
| Maximum Average Forward Rectified Current 0.375" (9.5mm) lead length at Ta=75 C                                    | I <sub>F(AV)</sub> | 1.5         |            |            |            |            |            |            |            |            | Α     |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)                   | I <sub>FSM</sub>   | 50.0        |            |            |            |            |            |            |            |            | А     |
| Maximum Instantaneous Forward Voltage at 1.5 A   | V <sub>F</sub>     | 1.1         |            |            |            |            |            |            |            |            | V     |
| Maximum DC Reverse Current $T_a = 25  ^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_a = 100  ^{\circ}\text{C}$ | I <sub>R</sub>     | 5.0<br>50.0 |            |            |            |            |            |            |            |            | μΑ    |
| Typical junction capacitance (NOTE 1)  | C <sub>j</sub>     | 20.0        |            |            |            |            |            |            |            |            | pF    |
| Typical thermal resistance (NOTE 2)  | $R_{\theta JA}$    | 50.0        |            |            |            |            |            |            |            |            | °C/W  |
| Operating and Storage Temperature Range  | $T_{j},T_{stg}$    | -55 to +150 |            |            |            |            |            |            |            |            | °C    |

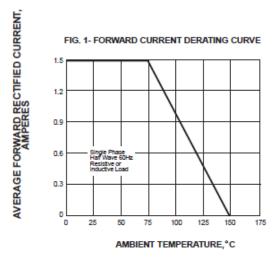
Note:1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

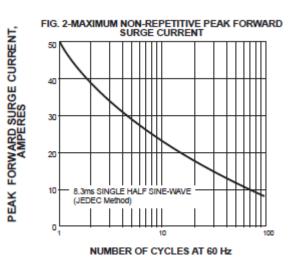
2.Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted

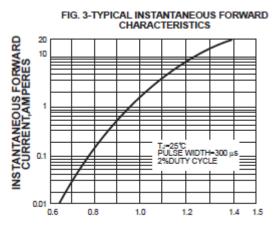
Dated:11/2019 Rev: 1.0

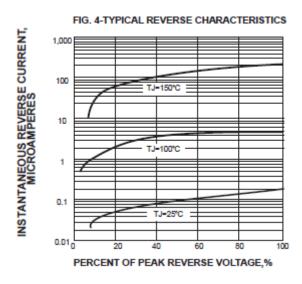
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### **RATINGS AND CHARACTERISTIC CURVES**

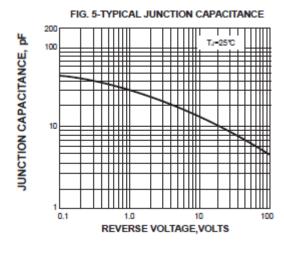


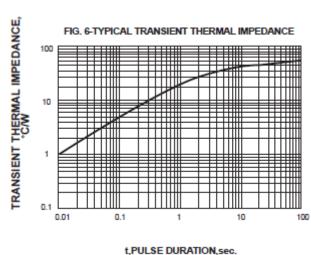






INSTANTANEOUS FORWARD VOLTAGE, VOLTS





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